



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Nathan Y. Moyal

Serial No.: 09/966,481

Filed: September 28, 2001

For: Generating Pulses for
Resetting Integrated Circuits

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Art Unit: 2816

Examiner: Hai L. Nguyen

Docket: ITL.0552US
P11111

Assignee: Intel Corporation

Mail Stop **Issue Fee**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**REPLY TO THE NOTICE OF DRAWING
INCONSISTENCY WITH SPECIFICATION**

Sir:

In response to the Notice of Drawing Inconsistency with Specification mailed July 21, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: August 1, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden
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